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## FACSIMILE TRANSMITTAL SHEET

In re application of: **Bor-Wen Chan, et al.**

Examiner: **Ron Everett Pompey**

Serial No.: **10/603,361**

Group Art Unit: **2812**

Filed: **06/25/2003**

Confirmation No.: **2523**

For: **METHOD OF MAKING A MULTIPLE GATE ELECTRODE ON A SEMICONDUCTOR DEVICE**

To: Examiner Ron Everett Pompey  
Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

FACSIMILE NUMBER: 571-273-8300

**CONFIRMATION**

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DATE: February 28, 2008

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MESSAGE: Attached is a Response to Office Action and a Petition for a One Month Extension (in duplicate).

NOTE: Original will not follow

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